

REMARKS/ARGUMENTS

Favorable reconsideration of this application, in light of the present amendments and following discussion, is respectfully requested.

Claims 21-40 are pending; Claims 1-20 are canceled; and Claims 21-40 are newly added herewith. It is respectfully submitted that no new matter is added by this amendment.

In the outstanding Office Action, Claims 7-12 were objected to for informalities; and Claims 1-20 were rejected under 35 U.S.C. § 102(b) as anticipated by Tamaki (U.S. Pat. No. 6,205,050).

With regard to the objection to Claims 7-12, that objection has been obviated by the cancellation of Claims 7-12.

The rejection of Claims 1-20 under 35 U.S.C. § 102(b) as anticipated by Tamaki has also been obviated by the cancellation of these claims.

Newly added Claim 21, from which Claims 22-32 depend, recites “a logic circuit which supplies the first address to the comparator in a normal operation mode and supplies the second address to the comparator in the test mode.” Similarly, Claim 33, from which Claims 34-40 depend, recites “replacing a defective cell with a redundancy cell when the input address is coincident with the defective address.”¹

Tamaki does not disclose or suggest supplying a first address to a comparator in a normal operation mode or supplying a second address to the comparator in the test mode. Additionally, Tamaki does not disclose or suggest replacing a defective cell with a redundancy cell when the input address is coincident with the defective address.

Accordingly, as Tamaki does not disclose or suggest the features of independent Claims 21 and 33, it is respectfully submitted that Claims 21-40 patentably distinguish over Tamaki.

¹ Support for new Claims 21-40 may be found, at least, in the specification at pages 25-27.

Consequently, in view of the foregoing discussion and present amendments, it is respectfully submitted that this application is in condition for allowance. An early and favorable action is therefore respectfully requested.

Respectfully submitted,

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